



# Ice Lake Platform Intel® MG PHY Firmware

**Release Notes - NDA**

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*Revision 7.0.3.0\_1045*

*January 2019*

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## Revision History

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Revision Number	Description	Revision Date
7.0.2.4	Initial Release	Jun 2018
7.0.2.6	Release	July 2018
7.0.3.0	Release	Nov 2018
7.0.3.0_1045	Release	Jan 2019

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# 1 Introduction

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## 1.1 Scope of Document

This document provides component level details of the downloaded kit and the contents of each folder in the kit.

## 1.2 Acronyms

Term	Description
BIOS	Basic Input Output System
FW	Firmware

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## 2 *Issue Status Definitions*

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This document provides sightings and bugs report for Intel® CSME Firmware 13.0 SKU for the Ice Lake Platform. At the time of a milestone release, this report will be distributed with the Intel® CSME Kit and will provide information on new issues and the status of old issues (replacing the Release Notes document).

**Closed Issues:** This category will only display closed issues within the current Intel® CSME Kit release. After each release, old issues will be dropped down to the “Archive” section and then new closed issues will take its place back up top for the next release. If an issue is posted in this section, it will indicate that the issue has been verified and fixed within the kit that is being released.

**Known Issues:** This category will display all Known Issues since the Alpha release and will remain in this section until fixed or noted otherwise. “Known Issues” are still under investigation and may or may not be root caused.

**Archive – Fixes in Previous Kits:** This category will display all closed issues that were closed in their respected kit#. This section will serve as a history of fixed issues.

**Sightings listed in this document apply to the Ice Lake SKU’s unless noted otherwise in this document or in the sightings tracking systems.**

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### 3 Intel® MG PHY New Features - RCR's

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RCR Number	Change Information
N/A	N/A

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## 4 Closed Issues

Issue Number	Description	Details	Fixed in Version
1806130434	USB/ Thunderbolt /DP impedance distribution issue	Impedance calibration was improved and meet expectation	7.0.2.6
1305681297	USB/ Thunderbolt Receiver marginality at corner case	Receiver marginality at corner case improved by additional configurations	7.0.2.6
1305828070	Thunderbolt traffic may be lost after plug/unplug device while the system is in C state	Processor may fail to wake from C state while plug/unplug Thunderbolt device	7.0.3.0
1305808698	PCIe Link may hang when CLx enabled	Processor may fail to wake from C state or link training	7.0.3.0
-	Internal authentication update	-	7.0.3.0_1045

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## 5 Open / Known Issues – To Date

Issue Number	Description	Details	Affected Sku's
N/A	N/A	N/A	N/A

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## 6 Archive

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### 6.1 Fixed/removed in Previous Kits

Issue Number	Description	Details	Affected Sku's
1806148869	When TBT device is connected PKGC is limited to PKGC8.	TBT FW doesn't support CLx low states  <b>Comment: issue no longer under debug by MG PHY and move for debug by other component</b>	ICL-U B0

### 6.2 RCR's

N/A

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